



3U cPCI to PMC Passive Carrier

Product Description:

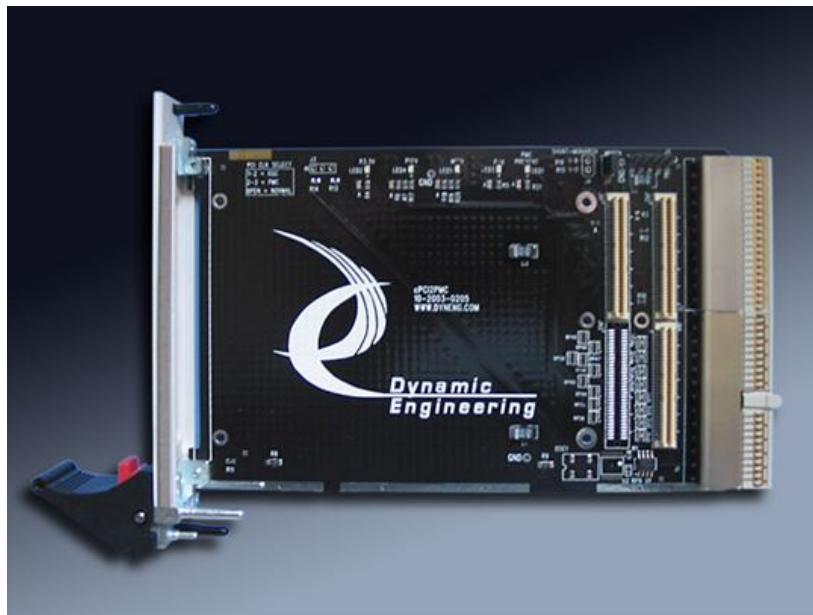
The cPCI2PMC-IO is a passive design incurring no added delays in connection from the PCI to the PMC buses. The design incorporates features to prevent the physical length of the traces from affecting operation. There may be cases where the backplane can't support all slots with cPCI2PMC cards installed. An active carrier is available for these applications.

The only standard shunt option on the cPCI2PMC is the one for M66EN. M66EN is pulled high on the backplane for each cPCI bus stub. If any device within that stub can't operate at 66MHz that device grounds M66EN to take the frequency to 33 MHz. The cPCI pin, PMC pin and shunt are tied together to allow the PMC, shunt or another device to select the frequency of operation. Please note that the master must be 66 MHz capable for any of the devices to operate at that frequency. The shunt is clearly marked on the silk screen.

The JTAG pins on the PMC are brought to a header for convenience. The pin definitions are in the silk screen. The JTAG pins on the cPCI connector are not recommended for use [per cPCI specification] and are not connected to the header. TDI is tied to TDO at the cPCI connector.

PrPMC devices are sometimes operated in "Monarch Mode" where the PMC drives the PCI clock. The Revision E fab has a new feature which is the addition of an optional location for an oscillator and clock buffer. The backplane can drive the PMC clock, the oscillator can source the backplane are assembled. J3 is used to select between the oscillator, PMC, and backplane clock when the monarch mode hardware is installed. J3 (1-2) selects the oscillator, J3 (2-3) selects the PMC and open = backplane.

The 5V, +12 and -12V voltages are supplied to the PMC slot via the cPCI connector. The voltages are bypassed at the cPCI connector and at the PMC connector. The 5V power has additional decoupling to support the regulator requirements.



Top view of cPCI2PMC-IO carrier

Ordering

PCI_cPCI2PMC

3U cPCI to PMC Carrier, passive 32 Bit, with front and rear I/O



PCI PMC Carrier Specifications

Key Features

- LED on PMC Busmode "Present"
- LED on plus 12
- LED on minus 12
- LED on plus 5
- LED on 3.3V
- 32 bit PCI operation
- Monarch mode options to provide or drive the PCI clock
- 33 or 66 MHz operation
- M66EN jumper option
- 10 ohm series resistors on AD31-0
- Zero delay buffer for PCI clock distribution
- Front panel connector access through cPCI bracket
- User IO [Pn4] available through J2
- Passive design for optimal electrical and thermal performance of PMC
- Power consumption: for the PMC site cannot exceed 25W.
- Operating Temperature: 0 to 50 °C
- Storage Temperature: -55 to 105 °C
- Operating Humidity: 10% to 80% (non-condensing).
- 1 year limited warranty